VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY, BURLA

Dated: 06.04.2021

NOTICE

No. VSSUT/Exams./ 680 /2021,

The Backlog subjects of Odd End Semester Examinations (**Except Final Year**) of **November-2020** will be conducted from 12.04.2021.

The detailed Day, Timing and Programme for all the courses are given below.

1ST SEMESTER (B.Tech.)

DATE & TIME	DAY	SUBJECT(S)
14.04.2021	Wednesday	Mathematics-I (B.Tech)
7.30 AM to 10.30 AM		
19.04.2021	Monday	1) Physics (B.Tech.)
7.30 AM to 10.30 AM		2) Chemistry (B.Tech.)
22.04.2021	Thursday	1) Basic Electrical Engineering (B.Tech.)
7.30 AM to 10.30 AM		2) Basic Electronics (B.Tech.)
	Tuesday	1) Engineering Mechanics (B.Tech.)
27.04.2021		2) Programming for Problem Solving (B.Tech.)
7.30 AM to 10.30 AM		3) Computer Programming (B.Tech.)
		4) Programming & Data Structure (B.Tech.)
30.04.2021 7.30 AM to 10.30 AM	Friday	1) English for Business Communication (B.Tech.)
		2) Basics of Civil Engineering (B.Tech.)
		3) Environmental Science (B.Tech.)
		4) English for Communication (B.Tech.)
		5) Environmental Science & Engineering (B.Tech.)

3RD SEMESTER (B. Tech.)

DATE & TIME	DAY	SUBJECT(S)
12.04.2021 7.30 AM to 10.30 AM	Monday	1) Mathematics- III (All Branches)
15.04.2021 7.30 AM to 10.30 AM	Thursday	 Engineering Economics (B.Tech.) Engineering Economics & Costing (B.Tech.) Organizational Behaviour (B.Tech.)
20.04.2021 7.30 AM to 10.30 AM	Tuesday	 Objected Oriented Programming (B.Tech.) Basic Thermodynamics (ME) Network Theory (EE, EEE) Electrical Machines (ETC) Introduction to Physical Metallurgy (M&M) Basic Manufacturing Processes (PE)
23.04.2021 7.30 AM to 10.30 AM	Friday	 Mechanics of Materials (CE) Mechanics of Solid (ME) Electrical Machines – I (EE,EEE) Network Analysis & Synthesis (ETC) Digital Electronics Circuits (CSE/IT) Fuels, Furnaces & Refractories (M&M) Elements of Electrical Machines (PE) Chemical Process Technology (ChE)

28.04.2021		 Civil Engineering Materials & Construction (CE) Manufacturing Science & Technology-I (ME) Engineering Thermodynamics (EE,EEE,PE) Analog Electronics Circuits-I (ETC)
7.30 AM to 10.30 AM	Wednesday	5) Analog Electronics Circuits (ETC)6) Data Structure & Algorithms (CSE/IT)
		7) Data & File Structure (CSE/IT)8) Metallurgical Thermodynamics & Kinetics (M&M)11) Fluid Dynamics (ChE)

The Programme of 5th Semester B.Tech. and other courses remain same as that in our notice no. VSSUT/Exams/678/'2021 Dated: 06.04.2021. If any discrepancy found, please report to the Examination Section as early as possible preferably on or before dt. 09.04.2021.

Sd/-Controller of Examinations

Dt.: 06.04.2021

Memo No.: VSSUT/Exams./ 681(35) /'2021,

Copy to:- All HODs (**requested to communicate among the faculty members**)/ Prof. I/C Examinations/ Dean, Academic Affairs/ Dean, PGS&R/ Dean, Students Welfare/ Dean, Faculty & Planning (**request to kindly hoist in the University web site**) / Professor, T&P/ PIC, Electrical Maintenance/ ME-I/C/ University Notice Boards/ All Hall of Residence Notice Boards/ Medical Officer, VSSUT Dispensary/ PA to Vice-Chancellor for information of Hon'ble Vice Chancellor.

Sd/-Controller of Examinations