

## VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY, BURLA

## NOTICE

No.VSSUT/PGSR/

/2019

Date: 08 /11/2019

In pursuance to provision 15.6 of Ph.D Regulations, Mr. Sibarama Panigrahi, Regd. No.14060001 of Computer Science & Engineering Department shall defend his Ph.D thesis before the Viva-Voce Board consisting of all DRC members and External Examiner on 11.11.2019 at 10.30 AM in the Computer Science & Engineering Department Seminar Hall VSSUT, Burla.

"STUDIES ON MACHINE LEARNING TECHNIQUES FOR TIME SERIES FORECASTING". Thesis Title:

All the faculty members and the students of the University are invited to attend the above "Open Dean, PGS & R Viva-voce Examination".

Memo No.VSSUT/PGS&R/ 7458) /19 Copy to:

Date: 08 /11/2019

- 1. HOD, Computer Science & Engineering for information and necessary action
- 2. Chairman DRC for information and necessary action
- 3. Dr.H. S. Behera, Associate Professor, of Information Technology, Supervisor for information and necessary action. The scholar be advised to attend the "Open Viva-Voce Examination" on appointed date and time complying with the instruction and make a presentation of his thesis, covering the background, objectives, methodology, results and the conclusions of his study.
- 4. All the members of DRC for information and necessary action.
- 5. All the Heads of the Departments with a request for wide circulation among the faculty members and students for attending the above viva voce.
- 6. The Controller of Examinations/Dean, Academic Affairs for kind information and necessary action with a request to attend the above viva voce.
- 7. Dean, Faculty & Planning for information with a request to hoist this notice in the University Website for information of all concerned.
- 8. P.A. to Vice-Chancellor, VSSUT for kind information of Vice-Chancellor. Hon'ble Vice Chancellor is requested to attend the above "Open Viva-Voce Examination".

VSS University of Technology Burla, Odisha, India-76801