VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY, BURLA

NOTICE

NO. VSSUT/ACD/

Dated:

The Time Table for Odd Semester UG and PG Programme of the year 2018 is notified as enclosed in Annexure-I to Annexure-VIII.

Annexure-I	:	1 st Semester B.Tech and B.Tech & M.Tech Dual Degree
Annexure-II	:	3 rd Semester B.Tech and B.Tech & M.Tech Dual Degree
Annexure-III	:	5 th Semester B.Tech and B.Tech & M.Tech Dual Degree
Annexure-IV	:	7 th Semester B.Tech and B.Tech & M.Tech Dual Degree
Annexure-V	:	B.Arch $(1^{st}, 3^{rd}, 5^{th}, 7^{th} \& 9^{th}$ Semester)
Annexure-VI	:	1 st Semester M.Tech
Annexure-VII	:	MCA (1 st , 3 rd & 5 th Semester)
Annexure-VIII	:	M.Sc. (1 st & 3 rd Semester)
		Integrated M.Sc. (1 st , 3 rd , 5 th , 7 th & 9 th Semester)

Dean, Academic Affairs

Memo. No.VSSUT/ACD/

Copy to:

- 1. University Notice Board
- 2. All HODs/ Wardens of All Hall of Residence/ Deans/ Prof.in-Charge/ ME/ I/c/ COE/ Workshop supdt. for information
- 3. PIC/ Co-ordinator, University Time Table Committee for information
- 4. Dean, F&P with a request to facilitate in hoisting of this Notice in University Website
- 5. PA to Vice-Chancellor for kind information of Hon'ble Vice-Chancellor.

Dean, Academic Affairs

Dated:

VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY, BURLA <u>NOTICE</u>

NO. VSSUT/ACD/

The classes of 8th semester B.Tech in Electrical Engg. branch in the room no. D-220 is temporally shifted to C-322 for Short Term Course on "Energy & Grid Integration (REGI)" from 2nd to 7th April 2018. This is for information of all concerned.

Memo. No.VSSUT/ACD/ Copy to: Dean, Academic Affairs Dated:

- 1. Concerned HOD for information and necessary action.
- 2. Concerned Hall of Residence for information.
- 3. PIC, University Time Table for information.
- 4. PA to Vice-Chancellor for kind information of Hon'ble Vice-Chancellor.

Dean, Academic Affairs

Dated: