VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY: BURLA



(Formerly University College of Engineering, Burla-Established by Govt. of Odisha in 1956 & Upgraded in 2009 to A State Govt. University Covered under Section 2(f) & 12(B) of UGC Act.)

P.O: Engineering College, Burla (Siddhi Vihar), Dist: Sambalpur Odisha -768018, INDIA Ph: 0663-2430211, Fax: 0663-2430204 Website: www.vssut.ac.in

No. VSSUT/PGSR/

/18

Dated: 04/07/2018

NOTICE

Sub: Written Test for admission into M.Phil Programme 2018.

The Written Test for admission into M.Phil Programme 2018 to be conducted on 18.07.2018 at 11.30 AM. Candidates are required to bring their original Photo Identity Card issued by Government/Institution for verification at the Examination Hall. The Written Test will be of a single paper consisting of 50 multiple choice questions of 50 minutes duration of Post Graduate level of the concerned subject. The Written Test will start at 11.30 AM. and the candidates are required to report at least 30 minutes before the commencement of examination. No candidate will be allowed to appear after 15 minutes of the commencement of "Written Test". No separate intimation for appearing the Written Test shall be sent to the candidates. The Student who will not able to appear the entrance test, his /she claim for admission shall be forfeited.

Memo No.VSSUT/PGSR/ 1425 /18
Copy to:

Sd/-Dean, PGS&R Date: **04/07/2018**

- 1. HOD, Physics/Chemistry & Mathematics for information and necessary action
- 2. Convener, Entrance Examination, M.Phil, 2018
- 3. Dean, Faculty & Planning with a request to display the notice in University website
- 4. Registrar/ C.O.F for information.
- 5. PA to V.C for kind information of Hon'ble Vice-Chancellor

Dean, PGS&R