VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY: BURLA



(Formerly University College of Engg., Burla-Established by Govt. of Odisha in 1956 & Upgraded in 2009 to A State Govt. University Covered under Section 2(f) & 12(B) of UGC Act.)

P.O: Engineering College, Burla (Siddhi Vihar), Dist: Sambalpur Odisha– 768018.INDIA

No.VSSUT/OOR/Rect./ 719

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Date: 20.07.2017

NOTICE

The following candidates have been shortlisted based on their performance in the screening (written) test conducted today i.e, 29.07.2017 at 10AM in VSSUT, Burla for the Post of Assistant Professor in Electrical Engineering. They are asked to appear the interview before the Selection Committee today i.e, 29.07.2017 at 2.00PM in the office chamber of the Vice-Chancellor, VSSUT. Burla.

Accordingly, the concerned candidates are required to report 1(one) hour before the commencement of the interview for verification of their original certificates in Golden Jubilee Seminar Hall (No. B-212).

SI.No.	Category	Roll No.	Name	
1	UR	EE04	K. Sujita Kumar Achary	
2		EE11	Abhishek Das	
3		EE18	Swadhin Kumar Hota	
4		EE21	Smrutirekha Samal	
5		EE48	Rajendra Narayan Senapati	
6		EE57	Prabhas Ranjan Naik	
1	ST	EE09	Yogesh Kumar Nayak	
2		EE20	Sujata Bhoi	
3		EE24	Rampreet Manjhi	
4		EE35	Bisaya Bhoi	

Memo. No. OOR/Rect/720(5)/17

Date - 29.07.2017

Registrar

CC:

1) University Notice Board.

2) PA to Registrar for information and record.

3) PA to VC for kind information of the Hon'bel Vice-Chancellor.

(4) Dean, F&P for information and necessary action.

5) University Website.

REGISTRAR
V.S.E. University of Technology
Buria, Sambelow, Ocksta-768078

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