



VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY BURLA

ବୀର ସୁରେନ୍ଦ୍ର ସାଏ ବୈଷୟିକ ବିଶ୍ୱବିଦ୍ୟାଳୟ

(A UGC Recognized State Government University by an Act of Assembly, Estd. -1956)

P.O. Engineering College, Burla, Dist: Sambalpur, Odisha, (India) -768 018

www.vssut.ac.in, e-mail: vc@vssut.ac.in

No. VSSUT/ACD/ 70 /2026

Dated: 02/02/2026

NOTICE

The following B.Tech & B.Arch admitted students in the year 2025 are required to collect their identity cards as per the following schedule at the Sir Visveswaraya Auditorium of the University.

Sl.No.	Branch	Date and Time
1.	B.Tech (Lateral Entry – All Branches)	02/02/2026 (5:00 PM to 6:00 PM)
2.	MCA	
3.	Computer Science & Engineering	03/02/2026 (5:00 PM to 6:00 PM)
4.	Computer Science & Engineering (AI&ML)	
5.	Safety & Fire Engineering	
6.	Mechanical & Smart Manufacturing	
7.	Electronics & TC Engineering	04/02/2026 (5:00 PM to 6:00 PM)
8.	Electrical Engineering	
9.	Mechanical Engineering	05/02/2026 (5:00 PM to 6:00 PM)
10.	Metallurgical & Materials Engineering	
11.	Electrical & Electronics Engineering	
12.	Architecture	
13.	Civil Engineering	06/02/2026 (5:00 PM to 6:00 PM)
14.	Chemical Engineering	
15.	Production Engineering	
16.	Biomedical Engineering	

Memo No.VSSUT/ACD/ 71 /2026
Copy to:

1. All Deans / HODs / Director, IQAC /COF for information.
2. Notice Board of Concerned Halls of Residence.
3. Dean, F&P with a request to facilitate in hoisting this notice in the University Website.
4. PA to Vice-Chancellor for kind information of Hon'ble Vice-Chancellor.


Dean, Academic Affairs
Dated: 02/02/2026

**Dean
Academic Affairs
VSSUT. Burla**


Dean, Academic Affairs

**Dean
Academic Affairs
VSSUT. Burla**