VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY BURLA ବୀର ସ୍ଥରେନ୍ଦ୍ର ସାଏ ବୈଷୟିକ ବିଶ୍ୱବିଦ୍ୟାଳୟ



(A UGC Recognized State Government University by an Act of Assembly, Estd. -1956)
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No. VSSUT/Exam. / 981

/2025

Dated: 01/12/2025

NOTICE

The schedule for Repeat mid Semester Examination of B.Tech./ B.Arch. (1st Sem), MCA (1st Sem) of Academic Session 2025-26 to be conducted from 12.12.2025 is as follows. Students who are allowed for the test can appear it.

PROGRAMME FOR 1ST SEMESTER (B.Tech)

DATE & TIME	DAY	SUBJECT(S)
12.12.2025 3:00 to 4:30 PM	Friday	1) Mathematics-I (All Sections)
15.12.2025 3:00 to 4:30 PM	Monday	1) C & Data Structures (Sec. – M, N, O, P, Q, R, S & T) 2) Engineering Mechanics (Sec. A, B, C, D, E, F, G & H)
17.12.2025 3:00 to 4:30 PM	Wednesday	1) Basic Manufacturing Processes (Sec. – M, N, O, P, Q, R, S & T) 2) Basics Civil Engineering (Sec. A, B, C, D, E, F, G & H)
19.12.2025 3:00 to 4:30 PM	Friday	 English for Technical Writing (Sec. – M, N, O, P, Q, R, S & T) Universal Human Values (Sec. A, B, C, D, E, F, G & H)
22.12.2025 3:00 to 4:30 PM	Monday	1) Basic Electrical Engineering (Sec. – M, N, O, P, Q, R, S & T) 2) Basic Electronics (Sec. A, B, C, D, E, F, G & H)
24.12.2025 3:00 to 4:30 PM	Wednesday	1) Physics (Sec. – M, N, O, P, Q, R, S & T) 2) Chemistry (Sec. A, B, C, D, E, F, G & H)

Controller of Examinations

Memo No.: VSSUT/Exams. / 982 /2025

Dt.:01/12/2025

Copy to: - All HODs (requested to communicate the notice among the faculty members)/ Prof. I/C Examinations/ Dean, Academic Affairs/ Dean, PGS&R/ Dean, Students Welfare/ Dean, Faculty & Planning (requested to kindly hoist the notice in the University web site)/ Professor, T&P/ PIC, Electrical Maintenance/ ME-I/C/ University Notice Boards/ All Hall of Residence Notice Boards/ PA to Vice-Chancellor for information of Hon'ble Vice Chancellor.

Controller of Examinations