



VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY : BURLA

(Formerly University College of Engg., Burla- Established by Govt. of Odisha in 1956 & Upgraded in 2009 to a State Govt. University Covered under Section 2(f) & 12(B) of UGC Act.)

P.O: Engineering College, Burla (Siddhi Vihar), Dist: Sambalpur
Odisha- 768018.INDIA

No: VSSUT/SRIC/ /2023

Date: / /2023

ADVERTISEMENT FOR THE POSITION OF JUNIOR RESEARCH FELLOW

Applications are invited for the position of the Junior Research Fellow in a Core Research Grant project funded by Science and Engineering Research Board (SERB), Department of Science and Technology, Government of India in the Department of Electrical Engineering, Veer Surendra Sai University of Technology Burla, Sambalpur, Odisha. For more details visit the website: www.vssut.ac.in.

REGISTRAR

Memo No. VSSUT/SRIC/ 986⁽⁶⁾ /2023

25/05/2023

Copy to:

- 1) Director I&PR Department, Govt. Of Odisha, Bhubaneswar, with a request to publish the above advertisement in one issue of the Odisha daily edition (ALL ODISHA EDITION) of THE SAMAJ and THE TIMES OF INDIA using minimum space at I&PR approved/lowest rates on or before 01.06.2023. The bills may be sent to the registrar, VSSUT, Burla-768018 in TRIPLICATE along with copies of the paper in which the publication is made, for necessary payment.
- 2) The University/Department Notice Board for publicity.
- 3) Dean F&P, with a request to hoist the notice in the University web site www.vssut.ac.in for wide publicity.
- 4) S.O. Accounts, for information and necessary action.
- 5) Steno to Registrar for record.
- 6) Dean SRIC, for information and record.

REGISTRAR

REGISTRAR
V.S.S. University of Technology
Burla, Sambalpur, Odisha-768018



VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY : BURLA

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Odisha- 768018, INDIA

Advertisement for the post of Junior Research Fellow (One position) funded by Science and Engineering Research Board (SERB), Government of India

Applications are invited from eligible students for the position of Project Junior Research Fellow (one-position) funded by Science and Engineering Research Board (SERB), Department of Science and Technology, Government of India under the principal investigator Dr. Manish Tripathy, Associate Professor, Department of Electrical Engineering, Veer Surendra Sai University of Technology, Burla, Sambalpur, Odisha. Interested and eligible candidates are requested to send hard copy of completed application (Annexure -1) along with self-attested copies of supporting documents to the Registrar, Veer Surendra Sai University of Technology, Burla, Sambalpur-768018, on or before **30th June 2023** by speed/registered post/courier. The copy of the application should also be mailed to mtripathy_ee@vssut.ac.in and rksamal_ee@vssut.ac.in. The details of the project are as follows:

Name of the Project	DEVELOPMENT OF A COMPOSITE INDEX TO EVALUATE THE IMPACT OF RENEWABLE RESOURCE TIME SERIES DYNAMICS ON POWER SYSTEMS
Principal Investigator	Dr. Manish Tripathy
Co-Investigator	Dr. Rajat Kanti Samal
Qualification	Essential: M.Tech. in Electrical Engineering or equivalent Preferable: Specialization in area of power systems
Fellowship	Rs. 31000/- (Thirty one thousand) plus 10% HRA, per month for the duration of the project (initially appointed for one year).
Nature and duration of post	The appointment will be purely on temporary basis and will be co-terminus with the project. The PI and Institute reserve the right to terminate the engagement of the JRF at any time, if the performance is not found satisfactory. The person engaged shall not be entitled for any claim implicit or explicit for permanent absorption in the University.
Registration for PhD	As per the expected outcomes of the project, the JRF will be required to register for PhD in the Dept. of Electrical Engg., VSSUT under the supervision of PI and Co-PI.

General Term and Condition:

1. Interested and eligible candidates are requested to send hard copy of completed application (Annexure -1) along with self-attested copies of supporting documents to the, **Registrar, Veer Surendra Sai University of Technology, Burla, sambalpur-768018, on or before 30th June 2023 by speed/registered post/courier.** University will not be responsible for any postal delay of application. Applications received after the above mentioned date will be treated as cancelled. On the envelope please inscribe “**Application for the post of Junior Research Fellow (SERB, Govt. of India), Department of Electrical Engineering, VSSUT Burla**”.
2. Candidates will be short listed for the interview based on the specialization and availability of candidates and other requirements as per the project.
3. The date and time of interview will be informed via email or phone and only short listed candidates will be allowed to attend the interview.
4. Candidates shortlisted for interview should appear in person with originals of certificates (one set of Xerox), and date of birth proof along with any other relevant information (like copies of publications, awards etc.)
5. No travelling or any other allowances is admissible for attending the interview.
6. Candidates will not be considered in absentia.
7. The Principal Investigator of the project reserves the right to fill or not fill position or withdraw the notification at any point of time and in any conflict the decision of PI and Institute will be final.
8. All legal implications are subjected to local jurisdiction only.

Sd/
(Dr. Manish Tripathy)

Principal Investigator

ANNEXURE-1

Application form for the Post of Junior Research Fellow (JRF) funded by **Science and Engineering Research Board, Govt. of India**

Personal Details:

1. Full Name (In capital):
2. Date of Birth (DD/MM/YY) :
3. Category (GEN/SC/ST/OBC)
4. Marital Status (Single/Married)
5. Gender (Male/Female)
6. Nationality
7. Address for Communication
8. Permanent Address
9. Mobile/Phone No.
10. Email:

Affix a recent
passport size
photo

Details of School/University/Institute Studied (From Matriculation Onward)

Degree	Discipline	University/Institute	Year	%Marks /CGPA	Division
Class X					
Higher Secondary					
B.Tech					
M.Tech					
Any other					

Qualifying Examination (GATE/CSIR-NET/ICAR-NET/UGC-NET) if any:

Professional Experiences (Teaching/Research/Industrial) if any:

Name of Organization	Designation	Nature of Work	From	To
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B.Tech Project/M.Tech Thesis Title:

Seminar/workshop/conference attended (use extra sheet if needed):

Name of Seminar/Conference etc	Paper presented	Organized by

Research Publication including book and book chapters (use extra sheet if needed)

Awards, patents, prizes etc (use extra sheet if needed):

Any other Relevant Information (use extra sheet if needed):

DECLARATION:

I hereby declare that I have carefully read the instructions and particulars supplied to me and that the entries made in this application form are correct to the best of my knowledge and belief. If selected for admission, I promise to abide by the rules and discipline of the Institute. I note that the decision of the Institute is final in regard to selection. The Institute shall have the right to expel me from the Institute at any time after my engagement, provided it is satisfied that I was admitted on false particulars furnished by me or my antecedents prove that my continuance in the Institute is not desirable. I agree that I shall abide by the decision of the Institute, which shall be final.

Place:

Date:

Signature of Applicant