

VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY, BURLA

P.O: Engineering College, Burla
(Siddhi Vihar), Dist: Sambalpur
Odisha-768018, INDIA



Formerly University College of Engg. Burla-Established by Govt. of Odisha in 1956 & Upgraded in 2009 to A State Govt. University Covered under Section 2(f) & 12(B) of UGC Act.) State Govt. University Covered under Section 2(f) & 12(B) of UGC Act
Upgraded in 2009 to A State Govt. University Covered under Section 2(f) & 12(B) of UGC Act.)

No. VSSUT/PGS&R / 131 /2022

Date:01.02.2022

NOTICE

In pursuance to provision 15.6 of Ph.D. Regulations, **Mr. Pratap Chandra Nayak**, Regd. No. **1710050001**, Ph.D. Scholar, Department of **Electrical Engineering** shall defend his Ph.D. thesis before the Viva-Voce Board consisting of all DRC members and External Examiner on **10.02.2022** at **10.30 AM** in the Seminar Hall of **Electrical Engineering** Department. The Viva-Voce Board will conduct the evaluation with the External Examiner conducting the Viva-Voce from his place via Google-Meet online platform hosted by the HOD, **Electrical Engineering** Department.

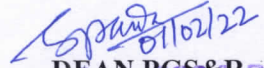
THESIS TITLE:“ SOME STUDIES ON FREQUENCY REGULATION OF CONVENTIONAL AND MICRO-GRID INTER CONNECTED POWER SYSTEMS BY ARTIFICIAL INTELLIGENCE TECHNIQUE BASED CONTROLLERS”

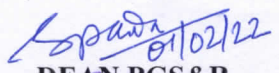
All the faculty members, staff and the students of the University are invited to attend the above “Open Viva-Voce Examination” by adopting social distancing and other precautionary measures as per COVID-19 Regulations. The interested Ph.D./ PG students and faculty members may attend the Viva-Voce via Google-Meet online platform. They may contact the HOD, Dept of **Electrical Engineering** for the link.

Memo No: VSSUT/PGS&R/132/22

Copy to:

1. **HOD, Electrical Engineering/ Chairman DRC**, for information and necessary action. He is requested to arrange to record the complete proceedings of the online Ph.D. Viva-Voce and hand over the records to Dean PGS&R office.
2. **Dr. Ramesh Chandra Prusty**, Assistant Professor, Department of **Electrical Engineering - Supervisor of the candidate and Convener DRC for information and necessary action**. The Scholar be advised to attend the “Open Viva-Voce Examination” on appointed date and time complying with the instruction and make a presentation of his thesis, covering the **background, objectives, methodology, results and the conclusions** of his study.
3. All the members of DRC for information and necessary action.
4. All the Heads of the Departments with a request for wide circulation among the faculty members and students for attending the above.
5. The Controller of Examinations/ Dean, Academic Affairs for kind information and necessary action with a request to attend the above.
6. Dean, Faculty & Planning for information with a request to hoist this notice in the University Website for information of all concerned.
7. P.A. to Vice-Chancellor, VSSUT for kind information of Vice-Chancellor. Hon'ble Vice Chancellor is requested to attend the above “Open Viva-Voce Examination”.


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VSSUT University of Technology,
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