## VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY, BURLA

## **NOTICE**

No. VSSUT/Exams./ 1249 /2021,

In Continuation to the notice VSSUT/ Exams/ 1221/ 2021, dated: 13.12.2021, the Regular and Backlog of Odd End Semester Examinations of 3<sup>rd</sup> Semester B.Arch./ Int. M.Sc., 5<sup>th</sup> B.Tech./ DD/ B.Arch./ Int. M.Sc., 7<sup>th</sup> Semester B.Arch./ Int. M.Sc. shall be conducted from **05.01.2022** instead of 06.01.2022. However, 3<sup>rd</sup> Semester B.Tech./ MCA, 1<sup>st</sup> Semester M.Tech./ M.Sc./ Int. M.Sc. shall be conducted from 06.01.2022.

Sd/Controller of Examinations

Dated: 20.12.2021

Memo No.: VSSUT/Exams./ 1250(35) /2021, Dated: 20.12.2021

Copy to:- All HODs (requested to communicate the notice among the faculty members) / Prof. I/C Examinations / Dean, Academic Affairs / Dean, PGS&R / Dean, Students Welfare / Dean, Faculty & Planning (requested to kindly hoist the notice in the University web site) / PIC, T&P / University Notice Boards / All Hall of Residence Notice Boards / PA to Vice-Chancellor for information of Hon'ble Vice Chancellor.

Sd/-Controller of Examinations VSSUT, BURLA