

# VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY, BURLA

## NOTICE

No. VSSUT/Exams./ 1221 /2021,

Dated: 13.12.2021

This for the all concerned that the exam schedule has been changed and the students appearing regular and backlog examinations are required to follow the guidelines as given below.

1. 3<sup>rd</sup> semester M.Tech. / M.Sc., 5<sup>th</sup> semester MCA, 7<sup>th</sup> semester B.Tech./DD, 9<sup>th</sup> semester Integrated M.Sc. subjects of odd End semester 2021 Examinations shall be conducted from 16/12/2021 in two sittings. The schedule of examination will be notified by tomorrow forenoon.
2. Rest of the end semester examinations shall be conducted from 06/01/2022 along with 1<sup>st</sup> semester M.Tech./ M.Sc./ Integrated M.Sc., and 3<sup>rd</sup> semester B.Tech./ MCA. The schedule for this shall be intimated in due course of time.
3. If due to some Unforeseen/ Pandemic situations the examinations are not conducted as per the schedule then the same examinations shall be conducted later in offline physical mode only.
4. The students who are appearing odd semester examinations from 06/01/2022 for them the even semester shall start from 17<sup>th</sup> January 2022 and the examinations shall be completed before 30<sup>th</sup> April 2022. All concerned are requested to take care of this matter seriously.
5. The students whose examinations are shifted are also advised to contact their subject teachers for extra classes and doubt clearing classes if required.

By order of Hon'ble Vice Chancellor,

Sd/-  
Controller of Examinations

Memo No. : VSSUT/Exams./ 1222 (35) /2021,

Dt.: 13.12.2021

Copy to:- All HODs (**requested to communicate the notice among the faculty members**) / Prof. I/C Examinations / Dean, Academic Affairs / Dean, PGS&R / Dean, Students Welfare / Dean, Faculty & Planning (**requested to kindly hoist the notice in the University web site**) / PIC, T&P / University Notice Boards /All Hall of Residence Notice Boards/ PA to Vice-Chancellor for information of Hon'ble Vice Chancellor.

Sd/-  
Controller of Examinations  
VSSUT, BURLA