



VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY, BURLA
SAMBALPUR, ODISHA - 768018
DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

No: VSSUT/EEE/722/2021

Date: 21.09.2021

NOTICE

This is for the information of all the interested candidates that a Walk-In Interview will be conducted in the Office of the Department of Electrical and Electronics Engineering (EEE) on dated: 29.09.2021. **All the Hiring shall be done through Outsourcing basis only.** Interested candidates may report at **E-201, Seminar Hall of the EEE department** along with CV and testimonials in support of their credentials **at sharp 10:00 AM on 29.09.2021.** They are required to bring their own pen, pencil etc. The written test and/or interview shall be conducted for the following post(s):

S. No.	Position	Number of posts	Minimum Qualification(s)
1.	Office Assistant	One (01)	Graduate Degree with skills in computational work.
2.	Junior Instructor (Electrical)	One (01)	Diploma in Electrical Engineering with skills in computational work.
3.	Junior Instructor (Electronics)	One (01)	Diploma in Electronics Engineering with skills in computational work.
4.	Lab Attendant	One (01)	Matriculation Certificate with Basic Computer knowledge.

* Computer Skill is essential for the S. No. 1 - 3. Candidates with the essential qualification, and having knowledge and skills in handling MS Office, prepare the official draft, and internet related activities will be given preference.

The written test shall be conducted for the S. No. 1 - 3 in the first half from 11:30 am – 12:30 am on 29.09.2021 followed by interaction with the committee members in the afternoon session from 02:30 pm – 04:30pm on the same day. Late reporting after 10:00am shall not be entertained for evaluation and interaction purposes in any circumstances.

NOTE:

- (1) All the above-mentioned positions shall be hired through Outsourcing basis only.
- (2) The entire process will be conducted strictly following the COVID guidelines. It is compulsory to produce the Final Covid Vaccination Certificate OR the RT-PCR test report generated within 72 hours from the date of appearing for the written test and/or interview.

HOD, EEE

Copy to: 1. All Notice Boards.

2. Circular file of the Department.

✓ 3. Dean F&P, VSSUT, Burla for advertisement through university website.

4. Registrar, VSSUT, Burla for kind information.

5. P.A. to the V.C. for kind information of the Hon'ble Vice Chancellor.


21/09/21
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