



VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY, BURLA

NOTICE


No.VSSUT/PGSR/ 763/2019

Date: 11/11/2019

In pursuance to provision 15.6 of Ph.D Regulations, Ms. Sarita Samal , Regd. No.14060009 of Electrical Engineering Department shall defend her Ph.D thesis before the Viva-Voce Board consisting of all DRC members and External Examiner on **14.11.2019 at 10.00 AM** in the Electrical Engineering Department Seminar Hall VSSUT, Burla.

Thesis Title: "POWER QUALITY IMPROVEMENT OF A MICROGRID SYSTEM USING UNIFIED POWER QUALITY CONDITIONER".


All the faculty members and the students of the University are invited to attend the above "Open Viva-voce Examination".


Dean, PGS & R

Memo No.VSSUT/PGS&R/ 764(8) /19
Copy to:

Date: 11/11/2019

1. HOD, Electrical Engineering for kind information and necessary action
2. Chairman DRC for kind information and necessary action
3. Dr. P K Hota, Professor, Electrical Engineering, VSSUT, Supervisor for information and necessary action. The scholar be advised to attend the "Open Viva-Voce Examination" on appointed date and time complying with the instruction and make a presentation of her thesis, covering the **background, objectives, methodology, results and the conclusions** of her study.
4. All the members of DRC for information and necessary action.
5. All the Heads of the Departments with a request for wide circulation among the faculty members and students for attending the above viva voce.
6. The Controller of Examinations/Dean, Academic Affairs for kind information and necessary action with a request to attend the above viva voce.
7. Dean, Faculty & Planning for information with a request to hoist this notice in the University Website for information of all concerned.
8. P.A. to Vice-Chancellor, VSSUT for kind information of Vice-Chancellor. Hon'ble Vice Chancellor is requested to attend the above "Open Viva-Voce Examination".


11.11.2019
Dean, PGS & R

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