



**VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY, ODISHA**  
**P.O: Engineering College Burla (Siddhi Vihar), Dist – Sambalpur**  
**Odisha – 768018, India**

Website: [www.vssut.ac.in](http://www.vssut.ac.in) , Email: [info@vssut.ac.in](mailto:info@vssut.ac.in) , PH: (0663)2430211,  
 Fax:(0663)2430204

No. VSSUT/Elect/114/2019

Date: 06/11/2019

**Advertisement for Junior Research Fellow (JRF)**

Applications are invited from highly motivated and bright candidates for engagement as **Junior Research Fellow** in the research project funded by **Central Power Research Institute (CPRI), Bangalore, Ministry of Power (MoP), Govt. of India, under the supervision of Dr. Gyan Ranjan Biswal, Principal Investigator (PI), Dept. of Electrical and Electronics Engineering, Veer Surendra Sai University of Technology (VSSUT), Burla.** Interested candidate may upload their duly filled application form in prescribed format (Annexure-I) through Hyperlink provided to fill the Google Spreadsheet(s) [ <https://forms.gle/ReFKDsqMrpKqpJMr> ] along with self-attested copies for the proof of qualifications, experience and other relevant information. Application must be submitted/ uploaded on or before **23.11.2019 at 12.00 p.m.** Shortlisted candidates have to bring the hardcopy of application form along with proof of relevant documents at the time of interview. Application received after deadline will be rejected. For technical information of the project, the candidate may contact the PI. Details of the project are furnished below:

**Project Title:** IEC 61850 Complaint SF<sub>6</sub> Monitoring System for Gas Insulated Switchgear

**Project Duration:** ONE (01) Year OR till completion of the project OR till the availability of funding whichever earlier.

**Principal Investigator:** Dr. Gyan Ranjan Biswal, Principal Investigator (PI), Dept. of Electrical and Electronics Engineering, Veer Surendra Sai University of Technology (VSSUT), Burla, Email: [gyanbiswal@vssut.ac.in](mailto:gyanbiswal@vssut.ac.in) , Mobile No: 8284008360

**Name of the position:** Junior Research Fellow (JRF)

**Number of position:** One (01)

**Essential:**

P.G. in the discipline of Electrical Engineering/ Electronics and Instrumentation Engineering/ Instrumentation Engineering with specialization in Control and Instrumentation/ Power Systems Engineering with at least 60% in aggregate or 6.5 CGPA on 10 point scale.

**Desirable:** GATE qualification. Candidate with similar/related project experience may be given preference.

**Age limit:** Must not be over 28 years as on last date of application. Upper age may be relaxed up to 5 years in case of candidate belonging to SC/ST/OBC/Female/Differently abled candidate. (As per GOI)

**Salary:** Consolidated ₹ 31,000.00 (thirty one thousand only) per month for entire duration of project from the date of appointment.

**Date and Venue of Interview:** Date and venue of the interview will be communicated later to the short listed candidate through email communication, and display on university website notice.

**Terms and Conditions:**

1. This position is on temporary and contractual basis. This position is co-terminus with the project. Candidate selected for this position can also apply for the regular Ph.D. program of Veer Surendra Sai University of Technology (VSSUT), Burla, provided they satisfy eligibility criteria as per university Ph.D. regulations.
2. This position is initially for one year or till the availability of funding whichever is less, and is likely to be extended for subsequent period.
3. Candidates will be short listed for the interview based on merit and will be informed via email.
4. Original documents of age proof/certificates/degrees/mark sheets and other testimonials must be presented at the time of interview.
5. Mere fulfilment of the required qualifications and experiences does not entitle a candidate to be shortlisted or called for interview. The Principal Investigator of the project reserves the right to fill or not fill the position.
6. No TA/DA will be paid for attending the interview.
7. Reservation is applicable as per GOI norm.

Leah  
06.11.19  
Registrar

No. VSSUT/Elect/1142/2019

Date: /06/11/2019

**Copy to:**

Dean, Faculty & Planning with a request to hoist the advertisement in the university website [www.vssut.ac.in](http://www.vssut.ac.in) for publicity.

Leah  
06.11.19  
Registrar



**Annexure-I**

APPROVAL FOR RECRUITMENT OF PROJECT STAFF  
(JUNIOR RESEARCH FELLOW)

[Google link: <https://forms.gle/ReFKDsqMrpKqpJMr> ]

Paste a self-  
attested  
passport size  
recent photo  
here

1	Name of Funding Agency	Central Power Research Institute (CPRI), Bangalore	
2	Scheme of Project Grant	Research Scheme on Power (RSoP)	
3	Project Title	IEC 61850 Compliant SF6 Monitoring System for Gas Insulated Switchgear	
4	<b>Personal Details</b>		
a.	Name of the Applicant:		
b.	Father's/Husband's Name:		
c.	Correspondence Address:	<hr/> <hr/> <hr/> <hr/>	
d.	Email ID:		
e.	Mobile No.:		
f.	(i) Date of Birth:	(ii) Age (on the last date of application)	
	(i) Gender:	(ii) Nationality:	
g.	Languages (Speak, Write and Understand):		
h.	Category (strike any one):	GEN / SC/ ST/ OBC/ others	
i.	Is Differently-abled:		

*leaf*



5 Academic Qualifications (Provide details in chronological order)					
Examination passed	Board/ University	Year	Subject/ Discipline/ Specialization (if any)	CGPA	Mark in Percentage
10 <sup>th</sup> / H.S.C.					
12 <sup>th</sup> / I.Sc.					
Bachelors:					
Masters					

6 Other Details (You may attach extra sheets)					
a.	Whether GATE qualified (Provide details if any):				
b.	Project experience (Provide details if any)				
	<u>Project Title</u>	<u>Position</u>	<u>Institute</u>	<u>From</u> (dd/mm/yy)	<u>To</u> (dd/mm/yy)
c.	Research publication (Provide details if any)				
	<u>Author(s) name</u>	<u>Journal / Conference</u>	<u>Paper Title</u>	<u>Vol. (Issue), Page no.</u>	<u>Month/ Year</u>
d.	Any other relevant information (Provide details if any):				

7. **DECLARATION**

I am willing to work as Junior/Senior/Research Fellow/Associate in the research scheme entitled: IEC 61850 Compliant SF6 Monitoring System for Gas Insulated Switchgear.

If the Fellowship/Associate ship is awarded to me, I undertake to engage myself whole time for research under the guidance of the Principal Investigator of the scheme. I have read 'terms and conditions' of the advertisement and accept and agree to abide by this. I have noted that the Fellowship / Associate ship is co-terminus with the scheme.

I clarify that to the best of my knowledge and belief, the particulars given in the application are correct.

Place:

Date:

(Signature of the applicant)

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**Note:** Candidates are advised to submit the application form through the hyperlink provided to fill Google spreadsheet form only. Shortlisted candidates have to bring the hardcopy of application form along with proof of relevant documents at the time of interview.

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(For official use only)

Application Serial No:

Category:

Date and time of receipt of application:

Signature of official:

*Handwritten signature*