



VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY, ODISHA  
P.O: Engineering College Burla (Siddhi Vihar), Dist – Sambalpur Odisha –  
768018, India

Website: www.vssut.ac.in , Email: info@vssut.ac.in , PH: (0663)2430211,  
Fax:(0663)2430204

No. VSSUT/Elect/24/2019

Date: 16/01/2019

**Advertisement for the Senior Research Fellow (SRF)**

Applications are invited for the position of Senior Research Fellow (SRF) in the research project funded by Central Power Research Institute (CPRI), Bangalore, Ministry of Power (MoP), Govt. of India, under the supervision of Dr. Gyan Ranjan Biswal, Principal Investigator (PI), Dept. of Electrical and Electronics Engineering, Veer Surendra Sai University of Technology (VSSUT), Burla. For details visit [www.vssut.ac.in](http://www.vssut.ac.in).

Sd/-  
Registrar

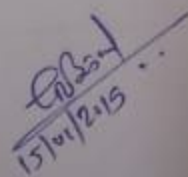
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**Copy to:**

- (1) M/S Display Line, 219, Sahid Nagar, Bhubaneswar for information with a request to publish the above Advt. in a single issue in daily Odia newspaper "The Samaj" on or before 26/01/2019.
- (2) Dean, Faculty & Planning with a request to hoist the advertisement in the university website [www.vssut.ac.in](http://www.vssut.ac.in) for publicity.
- (3) University notice board/Dept. Notice board for affixture.
- (4) The Registrar NIT Rourkela/ BPUT/ Sambalpur University/ Utkal University/ Berhampur University/ Ravenshaw University/ North Odisha University/ F.M. University/ OUAT/ G.M. University/ Rama Devi Women's University with a request to display the above advertisement in their notice boards.
- (5) HOD, EEE, for kind information and necessary action.
- (6) Dean SRIC, for kind information and necessary action.
- (7) COF, VSSUT, for information and necessary action. This is in reference to vide no. VSSUT/SRIC/CPRI PROJECT GR BISWAL EENGG., dated – 29/10/2018.
- (8) P.A. to Registrar, for kind information of Registrar.
- (9) P.A. to Vice-Chancellor, for kind information of Honourable Vice-Chancellor.

  
Registrar

  
15/01/2019



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No. VSSUT/Elect/ 24/2019

Date: 16/01/2019

**Advertisement for the Senior Research Fellow (SRF)**

Applications are invited from highly motivated and bright candidates for engagement as Junior Research Fellow in the research project funded by Central Power Research Institute (CPRI), Bangalore, Ministry of Power (MoP), Govt. of India, under the supervision of Dr. Gyan Ranjan Biswal, Principal Investigator (PI), Dept. of Electrical and Electronics Engineering, Veer Surendra Sai University of Technology (VSSUT), Burla. Interested candidate may send a Bio-data and Application form in prescribed format (Annexure-I) (along with self-attested copy for the proof of qualifications, experience and other relevant information) to “Dept. of Electrical and Electronics Engineering, Veer Surendra Sai University of Technology, Burla, PIN-768018, Sambalpur, Odisha, India” by speed/registered post only. Top of the envelope encapsulated application should be super scripted “**Application for the post of SRF in CPRI project of Dept. of Electrical and Electronics Engineering**”. Application must reach on or before **25/02/2019 at 4.30 p.m.** Application received in any other mode or received after due date will be rejected. For technical information of the project, the candidate may contact the PI. Details of the project are furnished below:

**Project Title:** IEC 61850 Complaint SF<sub>6</sub> Monitoring System for Gas Insulated Switchgear

**Project Duration:** 2 Years or till completion of the project whichever earlier.

**Principal Investigator:** Dr. Gyan Ranjan Biswal, Principal Investigator (PI), Dept. of Electrical and Electronics Engineering, Veer Surendra Sai University of Technology (VSSUT), Burla, Email: [gyanbiswal@vssut.ac.in](mailto:gyanbiswal@vssut.ac.in) , Mobile No: 8284008360

**Name of the position:** Senior Research Fellow (SRF)

**Number of position:** One (01)

**Essential:**

P.G. in the discipline of Electrical Engineering/ Electronics and Instrumentation Engineering/ Instrumentation Engineering or relevant specialization with at least 60% in aggregate or 6.5 CGPA on 10 point scale/ first class in all the degrees, and GATE qualification.

*G. Biswal*  
12/01/2019

**Desirable:** Should have sufficient exposure of Control and Instrumentation / Instrumentation and Control/ Instrumentation Engineering with research exposure in sensor designing area and the understanding of electrical engineering. Candidate with similar/ related project experience with emphasis on Sensor Technology may be given preference.

**Age limit:** Must not be over 30 years as on last date of application. Upper age may be relaxed up to 5 years in case of candidate belonging to SC/ST/OBC/Female/Differently abled candidate. (As per GOI)

**Salary:** Rs. 28000/- per month + HRA as per university norms for 1st year and 2nd year.

Note - As per guidelines directed by Govt. of India to funding agencies, research experience will count only after successful completion of masters' degree in engineering/ technology.

**Date and Venue of Interview:** Date and venue of the interview will be communicated later to the short listed candidate through email communication, and display on university website notice.

**Terms and Conditions:**

1. This position is on temporary and contractual basis. This position is co-terminus with the project. Candidate selected for this position can also apply for the regular Ph.D. program of Veer Surendra Sai University of Technology (VSSUT), Burla, provided they satisfy eligibility criteria as per university Ph.D. regulations.
2. This position is advertised for two years, subjected to satisfactory performance of fellow.
3. Candidates will be short listed for the interview based on merit and will be informed via email.
4. Original documents of age proof/certificates/degrees/ mark sheets and other testimonials must be presented at the time of interview.
5. Mere fulfilment of the required qualifications and experiences does not entitle a candidate to be shortlisted or called for interview. The Principal Investigator of the project reserves the right to fill or not fill the position.
6. If everything remains same, then SC/ST candidate may be given higher preference.
7. No TA/DA will be paid for attending the interview.
8. Reservation is applicable as per GOI norm.

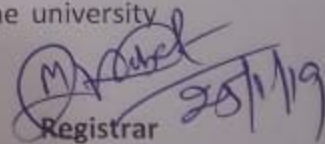
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Registrar

  
6/1/2019





VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY, BURLA Sambalpur,

Odisha, India. PIN:768018

PH:(0663)2430211, Fax:(0663)2430204, info@vssut.ac.in

No. VSSUT/Elect/24/2019

Date: 16/01/2019

**ADVERTISEMENT FOR SENIOR RESEARCH FELLOW**

Application are invited the position of **Senior Research Fellow (SRF)** funded by CPRI, Bangalore, MoP, Govt. of India in the Department of Electrical and Electronics Engineering on or before **25.02.2019** on or before 04:30 pm. For details visit the website: [www.vssut.ac.in](http://www.vssut.ac.in)

Sd-  
REGISTRAR

*CBoul*  
16/01/2019