



# VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY, BURLA

## NOTICE

No.VSSUT/PGSR/ 1602 /19

Date: 22 /01/19

In pursuance to provision 15.6 of Ph.D Regulation, Mr. Priyadarsan Parida, Regd. No. 13060028 of Electronics & Telecommunication Engineering Department shall defend his Ph.D thesis before the Viva-Voce Board consisting of all DRC members and External Examiner on **07.02.2019 at 11.30 AM** in the Seminar Hall of Department of Electronics & Telecommunication Engineering.

**Thesis Title:** “DEVELOPMENT OF TRANSITION REGION BASED METHODS FOR IMAGE SEGMENTATION”.

All the faculty members and the students of the University are invited to attend the above “Open Viva-voce Examination”.

  
Dean, PGS & R

Memo No. VSSUT/PGS&R/1603<sup>(7)</sup>/19

Dated: 22/01/19

**Copy to:**

1. HOD, Electronics & Telecommunication Engineering Department/Chairman DRC, for information and necessary action
2. Dr.Nilamani Bhoi, Associate Professor, Electronics & Telecommunication Engineering Department, Supervisor for information and necessary action.  
The scholar may be advised to attend the “Open Viva-Voce Examination on appointed date and time complying with the instruction and make a presentation of his thesis, covering the **background, objectives, methodology, results and the conclusions** of his study.
3. All the members of DRC for information and necessary action.
4. All the Heads of the Departments with a request for wide circulation among the faculty members and students for attending the above.
5. The Controller of Examination/Dean, Academic Affairs for kind information and necessary action with a request to attend the above.
6. Dean, Faculty & Planning for information with a request to hoist this notice in the University Website for information of all concerned.
7. P.A. to Vice-Chancellor, VSSUT for kind information of Vice-Chancellor.  
Hon’ble Vice Chancellor is requested to attend the above “Open Viva-Voce Examination”.

  
Dean, PGS & R