



**VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY,
BURLA**

PO- ENGINEERING COLLEGE BURLA, DIST-SAMBALPUR,
ODISHA-768018

No. VSSUT/MME/OURIIP - II/ 1911 /2024

Date: 20 /08/2024

QUOTATION CALL NOTICE

Sealed quotations are invited from registered firm with valid GST clearance for supply of a high speed computer to the **Dept. of Metallurgical & Materials Engineering**, VSSUT, Burla, Sambalpur, Odisha. The quotations shall reach the office of the undersigned through **Speed/Registered post only on or before 05.09.2024 up to 5.00PM.**

For more details, please visit our university website www.vssut.ac.in. The authority the reserve right to accept or reject all the tenders without assigning any reason thereof.

REGISTRAR

RAGAN

V.S.S. University of Technology
Burla

TIME SCHEDULE

- Date of commencement if downloading bidding document 21.08.24 at 11.00 AM.
- Last date and time for Receipt of bids 05.09.24 up to 5.00 PM.
- PLACE OF OPENING OF QUOTATION AND ADDRESS FOR COMMUNICATION AND RECEIPT OF BID DOCUMENTS:

**THE HOP, DEPARTMENT OF METALLURGICAL AND MATERIALS
ENGINEERING**

VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY, ODISHA

At- Burla, Po-Burla Engineering College, Dist-Sambalpur-768018,

Tel. No-0663-2430211 Fax No-0663-2430204

Ref. No. VSSUT/MME/OURIIP – II/ 1912 /2024

Date: 20/08/2024

Copy to:

- University Notice Board of VSSUT, Burla.
- The Dean F&P with a request to display the advertisement in university website.
- Comptroller of Finance for information and necessary action
- . PA to Registrar for information and necessary action.
- PA to Vice Chancellor for information of the Hon'ble Vice Chancellor.

REGISTRAR

Page 1 of 3
REGISTRAR

V.S.S. University of Technology
Burla



VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY: BURLA

ବୀର ସୁରେନ୍ଦ୍ର ସାଏ ବୈଷୟିକ ବିଶ୍ୱବିଦ୍ୟାଳୟ

(Established by Govt. of Odisha in 1956 & upgraded in 2009 to A State Govt. University Covered Under Section 2(f) & 12 (B) of UGC Act.)

P.O : Engineering College, Burla (Siddhi Vihar), Dist : Sambalpur Odisha -768018, INDIA

Ph : 0663-2430211, Fax : 0663-2430204 Website : www.vssut.ac.in

Ref: No: VSSUT/MME/OURIIP – II/2024/1911

Date: 20/08/2023

QUOTATION CALL NOTICE

Sealed Quotations are invited for supply of a high speed computer by the “The Registrar, Veer Surendra Sai University of Technology, Odisha” from registered firm with valid GST clearance for supply of a “*High Speed Computer*” to the Department of Metallurgical & Materials Engineering, VSSUT Burla, Odisha. The technical specification is mentioned below.

Specification

SI No.	Item	Specification
1.	Desktop Computer (High Speed)	<ul style="list-style-type: none">• Core i7• 13th Generation• 16GB RAM• 1 TB Hard Disk• 256 GB SSD• 4 GB Graphics card• OS Window 11• MS Office 21• 22 Inch Monitor• 1 Year warranty

Terms & Conditions:

1. The rate should be inclusive of all charges except GST. The GST must be indicated separately.
2. Brand/make of the items should be mentioned in the quotation along with details specifications, expiry date, warranty and leaf-let (if any)
3. The original bid document signed & scaled by authorized person in each page as a token of acceptance of all terms and conditions of the quotation with original receipt.
4. The hard copy of price bid giving the rates for one number of high speed computer should be submitted.
5. GST registration certificate/ PAN card (Xerox copy) certificate should be submitted along with the quotation.

6. Delivery of the Desktop Computer should be made in the Department of Metallurgical and Materials Engineering, VSSUT Burla, Sambalpur, Odisha.
7. If the supplier fails to supply with 30 days after receiving the purchase order, the order will automatically be cancelled.
8. The university authority reserves the right to reject or select any or all quotations without assigning any reason thereof.
9. The quotation cover should be superscribed with the words **“Quotation for Desktop Computer for Department of Metallurgical and Materials Engineering”**

Registrar
VSSUT, Burla
[Handwritten Signature]
REGISTRAR
V.S.S. University of Technology
Burla