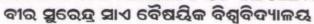
VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY BURLA





(A UGC Recognized State Government University by an Act of Assembly, Estd. -1956)
P.O. Engineering College, Burla, Dist: Sambalpur, Odisha, (India) -768 018
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No. VSSUT/ACD/ MY 2/202

Dated: 24/12/2024

NOTICE

The following B.Tech & B.Arch admitted students in the year 2024 are required to collect their identity cards as per the following schedule in the University Auditorium.

Sl.No.	Branch	Date and Time
1.	Chemical Engg.	26/12/2024 (10:00 AM to 1:00 PM) and (3:00 PM to 5:00 PM)
2.	Civil Engg.	
3.	Computer Sc. & Engg.	
4.	Computer Sc. & Engg. (AI & ML)	
5.	Metallurgy and Materials Engg.	
6.	Architecture	
7.	Electrical Engg.	27/12/2024 (10:00 AM to 1:00 PM) and (3:00 PM to 5:00 PM)
8.	Electrical & Electronics Engg.	
9.	Mechanical Engg.	
10.	MCA	
w.	Production Engg.	
12.	Electronics & TC Engg.	

Dean, Academic Affairs Dated: 24/12/2024

Memo No.VSSUT/ACD/ 1/3 /2024 Copy to:

1. All HoDs / HoPs for information.

2. Notice Board of Concerned Halls of Residence.

3. Dean, F&P with a request to facilitate in hoisting this notice in the University Website.

4. PA to Vice-Chancellor for kind information of Hon'ble Vice-Chancellor.

Dean, Academic Affairs