



VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY BURLA

ବୀର ସୁରେନ୍ଦ୍ର ସାଏ ବୈଷୟିକ ବିଶ୍ୱବିଦ୍ୟାଳୟ

(A UGC Recognized State Government University by an Act of Assembly, Estd. -1956)

P.O. Engineering College, Burla, Dist: Sambalpur, Odisha, (India) -768 018

www.vssut.ac.in. e-mail: vc@vssut.ac.in

No. VSSUT/ACD/ 740 /2024

Dated: 03/10/2024

NOTICE

The following 1st Semester B.Tech, B.Arch and MCA programme admitted students in the Academic Year 2024-25 are required to collect their registration cards as per the following schedule at Sir Visveswaraya Auditorium of the University.

| Sl.No. | Branch | Date and Time |
|--------|-----------------------------------|--------------------------------------|
| 1 | Electronics & TC Engg. | 04/10/2024 (4:00 PM to 5:30 PM) |
| 2 | Computer Sc. & Engg. (AI & ML) | 04/10/2024 (4:00 PM to 5:30 PM) |
| 3 | Metallurgical and Materials Engg. | 04/10/2024 (4:00 PM to 5:30 PM) |
| 4 | Mechanical Engg. | *05/10/2024 (12:00 PM to 1:00 PM) |
| 5 | Production Engg. | 05/10/2024 (12:00 PM to 1:00 PM) |
| 6 | Architecture | 05/10/2024 (12:00 PM to 1:00 PM) |
| 7 | MCA | 07/10/2024 (4:00 PM to 5:30 PM) |
| 8 | Chemical Engg. | 07/10/2024 (4:00 PM to 5:30 PM) |
| 9 | Civil Engg. | 07/10/2024 (4:00 PM to 5:30 PM) |
| 10 | Computer Sc. & Engg. | 08/10/2024 (4:00 PM to 5:30 PM) |
| 11 | Electrical Engg. | 08/10/2024 (4:00 PM to 5:30 PM) |
| 12 | Electrical & Electronics Engg. | 08/10/2024 (4:00 PM to 5:30 PM) |

Memo No.VSSUT/ACD/ 741 /2024

Copy to:

1. Notice Board of concerned Halls of Residence.
2. Dean, F&P with a request to facilitate in hoisting of this notice in the University website.
3. PA to Vice-Chancellor for kind information of Hon'ble Vice-Chancellor.

Dean, Academic Affairs

Dated: 03/10/2024

Dean, Academic Affairs