



VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY BURLA

ବୀର ସୁରେନ୍ଦ୍ର ସାଏ ବିଶ୍ୱବିଦ୍ୟାଳୟ

(A UGC Recognized State Government University by an Act of Assembly, Estd. -1956)

P.O. Engineering College, Burla, Dist: Sambalpur, Odisha, (India) -768 018

www.vssut.ac.in, e-mail: vc@vssut.ac.in

No. VSSUT/ACD/ 981 /2025

Dated: 16/09/2025

NOTICE

The following 1st Semester B.Tech, B.Arch, MCA & 3rd semester Lateral Entry (all Branch) programme admitted students in the Academic Year 2025-26 are required to collect their registration cards as per the following schedule at Sir Visveswararaya Auditorium of the University.

Sl.No.	Branch	Date and Time
1	Electrical & Electronics Engg.	17/09/2025 (4:00 PM to 6:00 PM)
2	Production Engg.	17/09/2025 (4:00 PM to 6:00 PM)
3	Computer Science & Engg. (AI ML)	17/09/2025 (4:00 PM to 6:00 PM)
4	Metallurgy and Materials Engg.	17/09/2025 (4:00 PM to 6:00 PM)
5	Mechanical & Smart Mfg. Engg.	17/09/2025 (4:00 PM to 6:00 PM)
6	Safety & Fire Engg.	17/09/2025 (4:00 PM to 6:00 PM)
7	Electrical Engg.	17/09/2025 (4:00 PM to 6:00 PM)
8	Electronics & TC Engg.	17/09/2025 (4:00 PM to 6:00 PM)
9	Computer Sc. & Engg.	18/09/2025 (4:00 PM to 6:00 PM)
10	Chemical Engg.	18/09/2025 (4:00 PM to 6:00 PM)
11	Bio-Medical Engg.	18/09/2025 (4:00 PM to 6:00 PM)
12	Mechanical Engg.	18/09/2025 (4:00 PM to 6:00 PM)
13	Civil Engg.	18/09/2025 (4:00 PM to 6:00 PM)
14	MCA	18/09/2025 (4:00 PM to 6:00 PM)
15	Lateral Entry (All Branch)	18/09/2025 (4:00 PM to 6:00 PM)

Memo No. VSSUT/ACD/ 982 /2025

Copy to:

1. Notice Board of concerned Halls of Residence.
2. Dean, F&P with a request to facilitate in hoisting of this notice in the University website.
3. PA to Vice-Chancellor for kind information of Hon'ble Vice-Chancellor.

Dean, Academic Affairs
Dated: 16/09/2025

Academic Affairs
VSSUT, Burla

Dean, Academic Affairs
Dean

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