## VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY BURLA

## ବୀର ସୁରେନ୍ଦ୍ର ସାଏ ବୈଷୟିକ ବିଶ୍ୱବିଦ୍ୟାଳୟ

Testavisto malor

(A UGC Recognized State Government University by an Act of Assembly, Estd. -1956)
P.O. Engineering College, Burla, Dist: Sambalpur, Odisha, (India) -768 018
www.vssut.ac.in. e-mail: vc@vssut.ac.in

No. VSSUT/PGS&R/ 1044

/ 2024

Date: 12/07/2024

## **NOTICE**

This is for information of all concerned that the Research Methodology & IPR (Odd Semester 2024) for all M.Tech (1<sup>st</sup> Semester) and Ph.D students enrolled in Spring 2024 and Autumn 2024 will be taken by following faculty members. Students are required to contact the concerned faculties for the commencement of the Classes from 15/07/2024 onwards.

Sl. No	Programme	School	Department(s)	Faculty
1	M. Tech	School of Infrastructure & Planning	Civil Engg.	Prof. J.P.Panda, Professor, Dept. of Mathematics
2	M. Tech	School of Computer Sciences, School of Chemical & Biosciences, School of Electrical Sciences, School of Humanities & Basic Sciences	EL & TC Engg., Electrical Engg, EEE, Mechanical Engg., Production Engg., MME	Dr. Madhusmita Panda, Asst. Professor, Dept. of Electronics & Telecom. Engg
3	Ph.D.	All	All	Prof. J.P.Panda, Professor, Dept. of Mathematics

Sd/-Dean, PGS&R

Memo No: VSSUT/PGSR/ 1045 Copy to: /24

Date: 12/07/2024

- 1) Prof. J. P. Panda, Professor, Dept. of Mathematics, for information and necessary action
- 2) Dr. Madhusmita Panda, Asst. Professor, Dept. of Electronics & Telecom. Engg, for information and necessary action.
- 3) All HoSs/HoPs/Deans, for information.
- 4) PA to VC for kind information of Hon'ble Vice-Chancellor.

Sd/-

Dean, PGS&R