



# VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY, BURLA

## NOTICE

No/VSSUT/PGS&R/ /19

Date: 07.05.2019

In pursuance to provision 15.6 of Ph.D Regulation, Mr. Deepak Kumar Patel Regd. No. 14060030 of Computer. Sc & Engg Department shall defend his Ph.D thesis before the Viva-Voce Board consisting of all DRC members and External Examiner on 10.05.2019 at 11 AM in the CSE Seminar Hall of VSSUT, Burla.

Thesis Title: "ON LOAD BALANCING MECHANISM WITH GRIDSIM"

All the faculty members and the students of the University are invited to attend the above "Open Viva-voce Examination".

Sd/-  
Dean, PGS & R

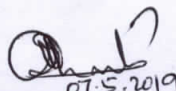
Memo No. VSSUT/PGS&R/ 265(8) /19  
Copy to:

Dated: 07.05.2019

1. HOD, Computer Sc. & Engg, VSSUT, Burla for information and necessary action
2. Chairman DSC for information and necessary action
3. Dr. C.R. Tripathy, Professor of Computer Sc. & Engineering, Supervisor for information and necessary action.

The Scholar may be advised to attend the "Open Viva-Voce Examination" on appointed date and time complying with the instruction and make a presentation of her thesis, covering the **background, objectives, methodology, results and the conclusions** of his study.

4. All the members of DRC for information and necessary action.
5. All the Heads of the Departments with a request for wide circulation among the faculty members and students for attending the above.
6. The Controller of Examinations/Dean, Academic Affairs for kind information and necessary action with a request to attend the above.
7. Dean, Faculty & Planning for information with a request to hoist this notice in the University Website for information of all concerned.
8. P.A. to Vice-Chancellor, VSSUT for kind information of Vice-Chancellor. Hon'ble Vice Chancellor is requested to attend the above "Open Viva-Voce Examination".

  
07.5.2019  
Dean, PGS & R