

SPONSORSHIP

Prof./Dr./Mr./Ms. _____
is an employee of our institute and his/her application is hereby sponsored. The applicant will be permitted to attend the short-term course on Signal Processing for Wireless Communication and Sensor Networks (**SPWCSN-2018**) at VSSUT, Burla to be held from 30-04-2018 to 05-05-2018

Signature of sponsoring authority
Date:

Office Seal Designation
DD No:
Date:
Bank:
Amount:

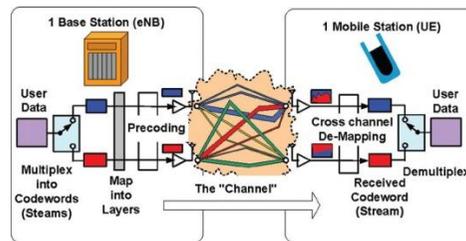
Signature of the applicant

The duly completed application should be mailed to:

**Dr. Arunanshu Mahapatro/
Dr. Harish Kumar Sahoo
Department of Electronics and
Telecommunication Engineering,
Veer Surendra Sai University of
Technology,
Burla, Sambalpur-768018, Odisha, India
Mobile: 9437438294, 9437086263
Email: arunanshu_etc@vssut.ac.in,
harish_etc@vssut.ac.in**

ABOUT US

Established on 12th Aug. 1956, at Burla in the name of University College of Engineering (UCE), the first engineering college was functioning as a constituent college of Utkal University, Bhubaneswar. The Veer Surendra Sai University of Technology (VSSUT) Odisha was formed by converting UCE to a non-affiliating Unitary University and came into force in the year 2009 by issue of notification by the Industries Department, Government of Odisha. The University occupies nearly 300 acres of prime land in Burla. With a glorious history stretching back over 61 years, providing technical education within a modern educational environment and strong academic staff, VSSUT is identified with engineering education in India. The University has a strong alumni base, most of them occupying coveted positions in many educational, industrial and research organizations all over the world.



Electronics and Telecommunication Engineering Department of VSSUT, BURLA offers an up-to-date 4 year B.Tech Degree course, 2 year M.Tech Degree course in Communication System Engineering, VLSI and Microwave Engineering and PhD in different specializations. The students graduated from this Department are well-placed in important National and International organizations.

AICTE QIP Short Term Course on Signal Processing for Wireless Communication and Sensor Networks (SPWCSN-2018) 30.04.2018 to 05.05.2018



**Coordinators
Dr. Arunanshu Mahapatro
Dr. Harish Kumar Sahoo**

Organized by



**Department of Electronics and
Telecommunication Engineering
Veer Surendra Sai University of
Technology, Burla.**

OBJECTIVE

Signal processing algorithms and architectures are significant both for signal processing and communication researchers to use their expertise in the rapid development of wireless communication systems and address the key challenges. The short term course is mainly focused on essential physical and network layer design issues and related signal processing solutions for a highly reliable and low-latency communications in indoor and outdoor environments. This short-term course **SPWCSN-2018** will bring together both signal processing and wireless communication research community to disseminate new developments and advances in cognitive radio and sensor networks.

COURSE CONTENT

- Fundamentals of MIMO-OFDM
- Modulation and Diversity Techniques
- Mathematical Model of the Time-Varying Wireless Channel
- Detection and Estimation Theory for Wireless Communication
- Opportunistic Spectrum Sensing
- Cognitive Radio Sensor Networks
- Distributed Estimation under Bandwidth and Energy Constraints
- Fault Tolerance in Sensor Networks

REGISTRATION DETAILS

No course fees charged for participants sponsored by AICTE approved institutions. However, a caution deposit of Rs.1000/- has to be paid, which will be returned when the participant joins for the course. All payments

are to be made in the form of demand draft drawn on any Nationalized Bank and in favor of "**Coordinator AICTE QIP STC on SPWCSN**" payable at Burla. Selection will be on first come first served basis and the number of participants is limited to 30. Interested candidates may send an advance copy of the completed application by email to avoid procedural/postal delay.

FINANCIAL ASSISTANCE

Limited number of participants from the AICTE approved institutions will be entitled up to III-AC to and fro railway fare* (via shortest route from the place of work).

*Tatkal charges will not be reimbursed.

FACILITIES

Boarding and lodging facilities will be provided for all the outside participants from AICTE approved institutions in the University guest house at Burla in twin sharing basis.

SPEAKERS

The course lectures shall be delivered by the faculty members of VSSUT, Burla and eminent speakers invited from IISc, IITs, NITs and other premier institutions of India.

ELIGIBILITY

The course is open to all teachers of degree level AICTE recognized technical Colleges/Universities/ Institutions.

Registration Form

**AICTE Sponsored Short Term Course
under QIP Scheme on
Signal Processing for Wireless
Communication and Sensor Networks
(SPWCSN-2018)
(30.04.2018 to 05.05.2018)**

- Name (Block Letters):
2. Designation &Dept:
3. Organization:
4. Address for communication:
Pin code:
Ph. No.:
E-mail:
5. Highest Academic Qualification:
6. Years of Experience:
7. Specialization:

Only for outside participants:

Draft No.----- Bank -----

This is certified that the above information furnished by me is correct to the best of my knowledge and belief.

Date: **Signature of the applicant**

Important dates:

Last date of receiving 15th March, 2018 applications:

List of shortlisted 1st April, 2018 participants:
(intimated through mail)