

VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY: ODISHA, BURLA

No.VSSUT/PGSR/1315 Dated: 22.09.2016

NOTICE

In continuation of this office Notice No.VSSUT/1200 dated 12.09.2016, the following Ph.D Scholars are provisionally allowed to appear the Odd Semester Examination 2016 for the course work subjects as mentioned against their names.

Sl. No.	Regn. No.	Name	Branch	Subject
1	1610050001	Debidasi Mohanty	EE	Research Methodology
	1010000001			2. Power System Analysis
2	1610050005	Nutan Saha	EE	Research Methodology Advanced Control System
				3. Dynamics of Electrical Machine
3	1610070001	Satyanarayan Rath	ETC	Research Methodology
				2. Microwave Circuits & Measurements
				3. Computational Electromagnetic
4	1610070002	Jogesh Chandra Dash	ETC	1. Research Methodology
				2. Optical & Satellite Communication
	1 < 1 0 0 7 0 0 0 2	M 1' IZ ' 1 D d	ETEC	3. Nano Science & Nano Technology (Chem)
5	1610070003	Murali Krishna Bonthu	ETC	1. Research Methodology
6	1610070004	Hrudananda Pradhan	ETC	1. Research Methodology
7	1610070005	Tunirani Nayak	ETC	Computational Electromagnetic Research Methodology
	1010070003	Tumam Nayak		Research Methodology Research Methodology
8	1610070006	Bikash Meher	ETC	2. Advanced Signal Processing
	1 1100 - 000 -			Research Methodology
9	1610070007	Dharamvir Kumar	ETC	2. Digital VLSI Design
10	1610040002	Sohan Kumar Pande	CSE	1. Research Methodology
				2. Real Time System
				3. Adv. in Data Structures & Algorithms
11	1610080001	Radha Mohan Pattanayak	IT	1. Research Methodology
				2. Real Time System3. Data Mining
				Research Methodology
12	1610140001	Amrita Nayak	PHY	2. Condensed Matter Physics-I
				3. Quantum Mechanics-I
13	1610120001	Gayatri Sabat	CHEM	1. Research Methodology
13	1010120001	Gayatii Savat	CHEN	2. Structure & Reactivity
14	1610120002	Muchipali Sailaja	CHEM	1. Research Methodology
				2. Structure & Reactivity
				Coordination Chemistry Research Methodology
15	1610150001	Anchal Kumawat	MCA	2. Discrete Mathematics
				3. Design of Computer Networks
				4. Object Oriented Analysis & Design
16	1610150002	Satyaban Behera	MCA	Research Methodology
				2. Discrete Mathematics
				3. Design of Computer Networks